From: 8064986673 To: USPTO Page: 6/10 Date: 2005/9/7 下午 01:52:56

Appl. No. 10/711,015 Amdt. dated September 07, 2005 Reply to Office action of June 30, 2005

Amendments to the Claims:

15

20

25

- (Currently amended) A copper damascene process, comprising: forming a dielectric layer overlying a substrate; etching a damascene_opening into said dielectric layer;
- filling said damascene opening with copper or copper alloy;
 treating a surface of said copper or copper alloy with hydrogen-containing plasma;
 reacting said treated surface of said copper or copper alloy with trimethylsilane or
 tertramethylsilane under plasma enhanced chemical vapor deposition (PECVD)
 conditions; and
- in-situ depositing, by PECVD, a silicon carbide layer capping on said copper or copper alloy.
 - (Original) The copper damascene process according to claim 1 further comprising: lining said damascene opening with a diffusion barrier layer; forming a seed layer on said diffusion barrier layer; and forming said copper or copper alloy on said seed layer.
 - 3. (Original) The copper damascene process according to claim 1 wherein said damascene opening comprises a contact or via hole in communication with a trench opening.
 - 4. (Original) The copper damascene process according to claim 1 wherein the step of reacting said treated surface of said copper or copper alloy with trimethylsilane or tertramethylsilane comprises following processing parameters: a trimethylsilane (or tertramethylsilane) gas flow in the range of 100 to 5000 sccm; a process temperature in the range of 300°C to 450°C; and a reaction duration in the range of 0.1 seconds to 30 seconds.

From: 8064986673 To: USPTO Page: 7/10 Date: 2005/9/7 下午 01:52:56

Appl. No. 10/711,015 Amdt. dated September 07, 2005 Reply to Office action of June 30, 2005

- (Currently amended) A copper damascene process, comprising: forming a dielectric layer overlying a substrate; etching a damascene opening into said dielectric layer; filling said damascene opening with copper or copper alloy;
- filling said damascene_opening with copper or copper alloy;
 treating a surface of said copper or copper alloy with hydrogen-containing plasma;
 reacting said treated surface of said copper or copper alloy with trimethylsilane or
 tertramethylsilane under plasma enhanced chemical vapor deposition (PECVD)
 conditions; and
- in-situ depositing, by PECVD, a silicon carbide layer capping on said copper or copper alloy, said layer silicon carbide layer being treated with in-situ ammonia plasma to remove contained oxygen of the deposited layer.
- (Original) The copper damascene process according to claim 5 further comprising:
 lining said damascene opening with a diffusion barrier layer;
 forming a seed layer on said diffusion barrier layer; and
 forming said copper or copper alloy on said seed layer.
- (Original) The copper damascene process according to claim 5 wherein said
 damascene opening comprises a contact or via hole in communication with a trench opening.
- 8. (Original) The copper damascene process according to claim 5 wherein the step of reacting said treated surface of said copper or copper alloy with trimethylsilane or tertramethylsilane comprises following processing parameters: a trimethylsilane (or tertramethylsilane) gas flow in the range of 100 to 5000 sccm; a process temperature in the range of 300°C to 450°C; and a reaction duration in the range of 0.1 seconds to 30 seconds.